

Welcome to RD ACCESS - Access to research results on semiconductor design

Project objectives

The objective of R&D ACCESS is to identify R&D results on semiconductor design from FP7 projects and to provide these results to partners from outside the consortia. The R&D results are divided into four categories:

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- Training and Education
- Intellectual Properties
- Design Methodologies
- Design Tools

Later R&D ACCESS will also include results originating from FP6, ENIAC, ARTEMIS and national programmes

What are the benefits for the user?

- ACCESS to European R&D knowledge infrastructure with initially more than 20.000 items on training, IPs, tools and methodologies
- ACCESS to one-shop-shop with single sign-on for semiconductor design knowledge
- ACCESS to annual workshops on application specific nanoelectronics platforms supporting the design in advanced processes

What are the benefits for the provider?

- A pan European dissemination platform
- Mailing lists reaching >40.000 semiconductor designers
- Support for design flow integration including training, IP, EDA tools and design methodologies

