VisualHDL: Bringing C++ productivity to VHDL world

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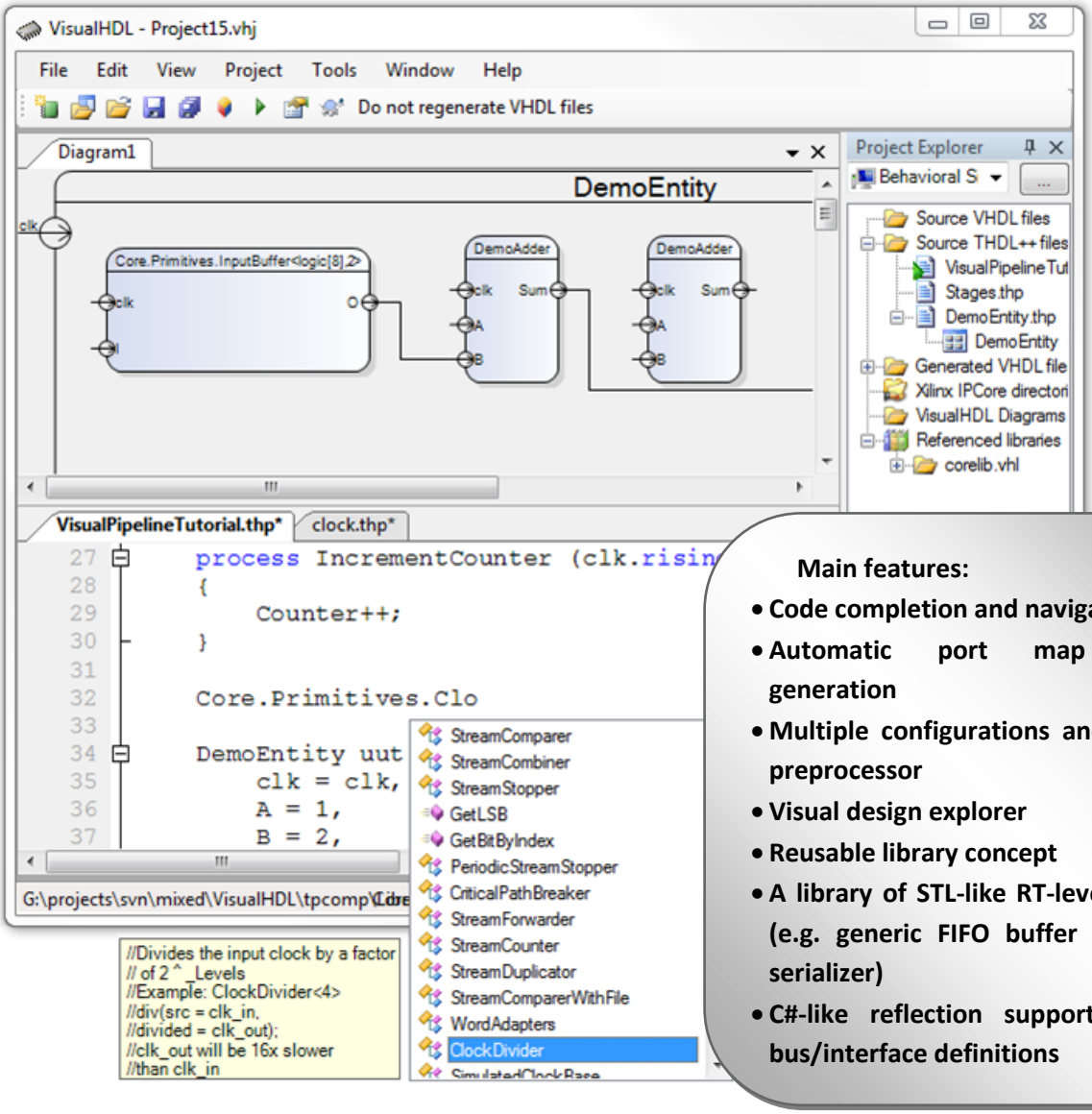
C++: high-productivity, reusable, maintainable, tons of IDEs

VHDL: full control, massively parallel, industry standard

Idea: Combine the best from both worlds!



Main features:

- **Code completion and navigation**
- **Automatic port map template generation**
- **Multiple configurations and a C++-like preprocessor**
- **Visual design explorer**
- **Reusable library concept**
- **A library of STL-like RT-level templates (e.g. generic FIFO buffer with record serializer)**
- **C#-like reflection supporting generic bus/interface definitions**

The tool is freely available online under http://visualhdl.sysprogs.org/