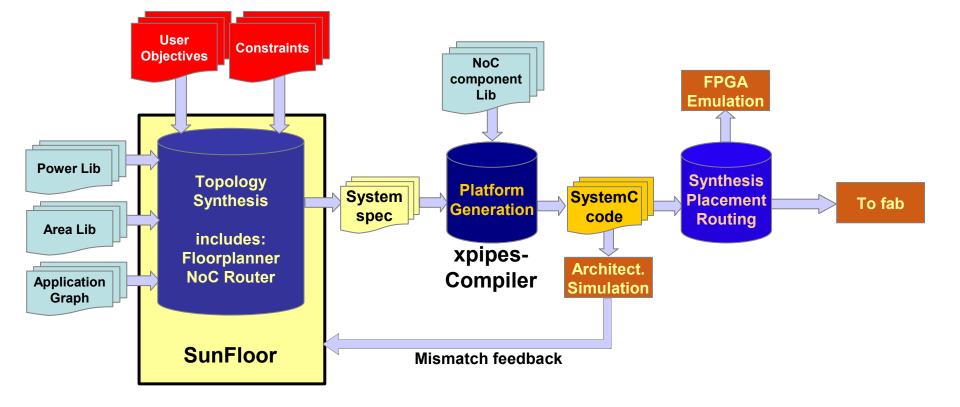
SunFloor NoC Synthesis Flow

 A complete flow for generation of NoC fabrics, from application to verification and floorplan

Optimization objectives: combination of power and hop delay
Optimization constraints: area, power, wirelength, hop delay



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